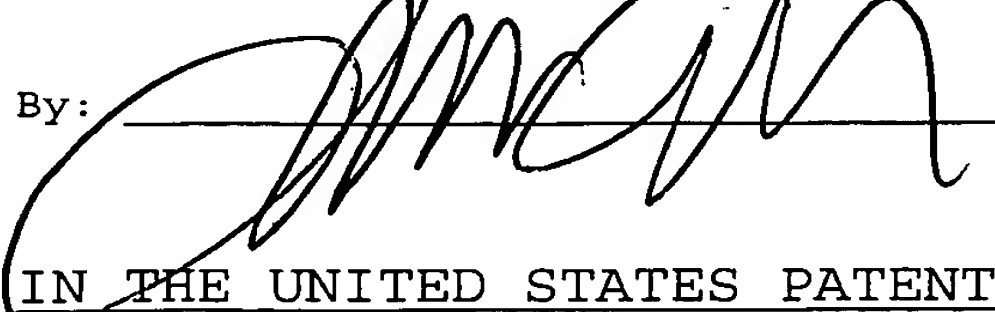


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P2000,0257

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450:

By: 

Date: November 4, 2003

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applic. No. : 10/033,123 Confirmation No. 7092  
Applicant : Heiko Fibranz et al.  
Filed : October 22, 2001  
TC/A.U. : 2816  
Examiner : Hai L. Nguyen  
Title : Integrated Circuit Having a Synchronous and  
an Asynchronous Circuit and Method for  
Operating Such an Integrated Circuit

Docket No. : P2000,0257  
Customer No. : 24131

Hon. Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**AMENDMENT**

S i r :

In response to the Office action dated August 4, 2003, please amend the above-identified application as follows:

**Amendments to the Specification** begin on page 2 of this paper.

**Amendments to the Drawings** begin on page 5 of this paper.

**Remarks/Arguments** begin on page 6 of this paper.

An **Appendix** including relabeled drawing pages 1/3 - 3/3 is attached following page 10 of this paper.

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